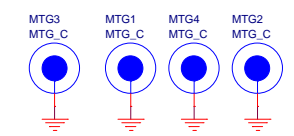
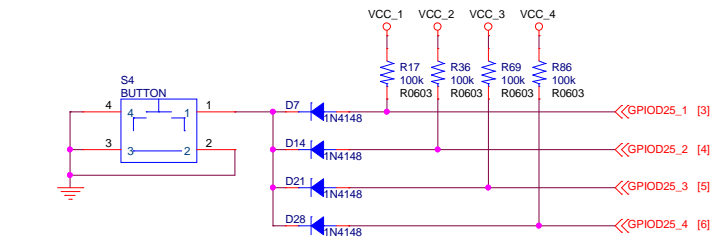
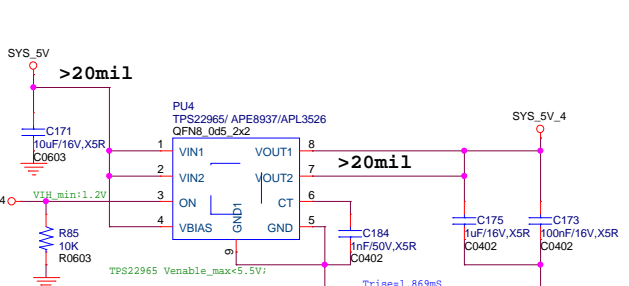
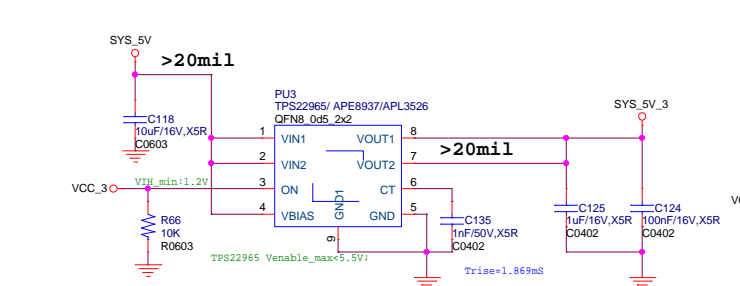
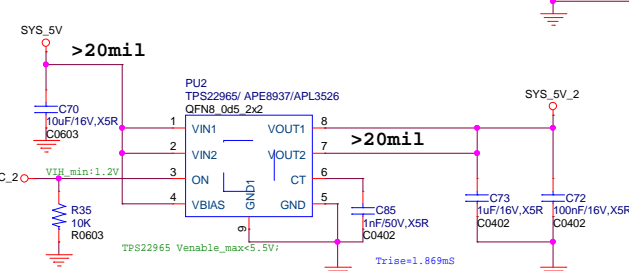
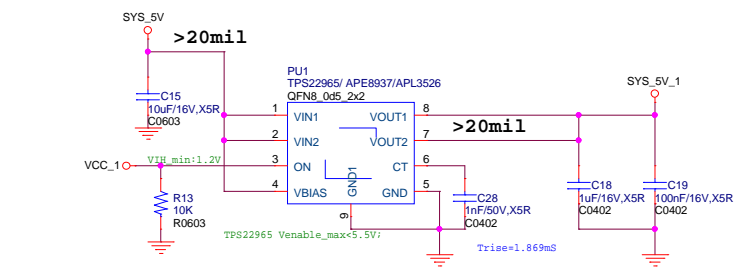
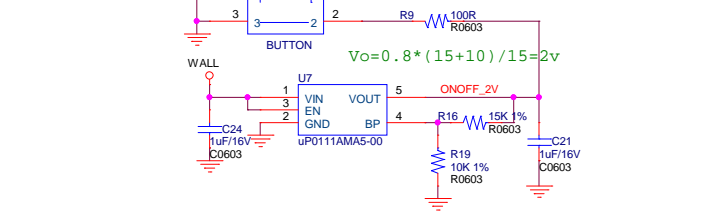
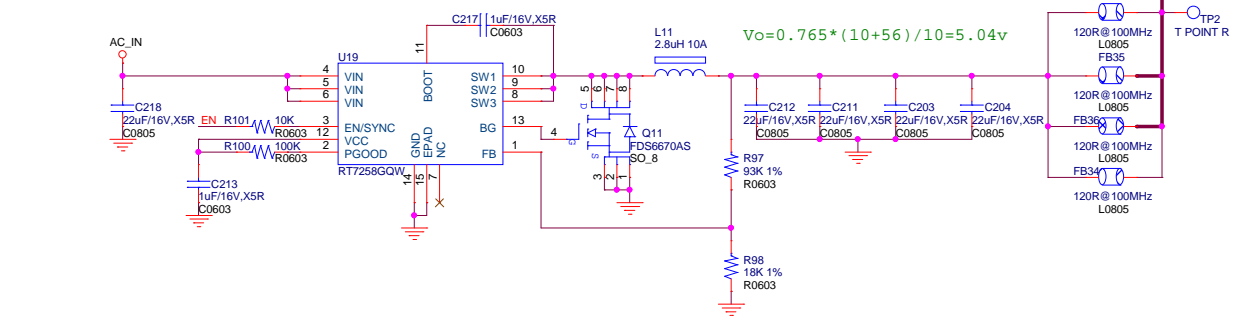
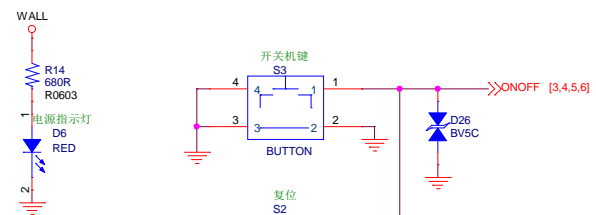
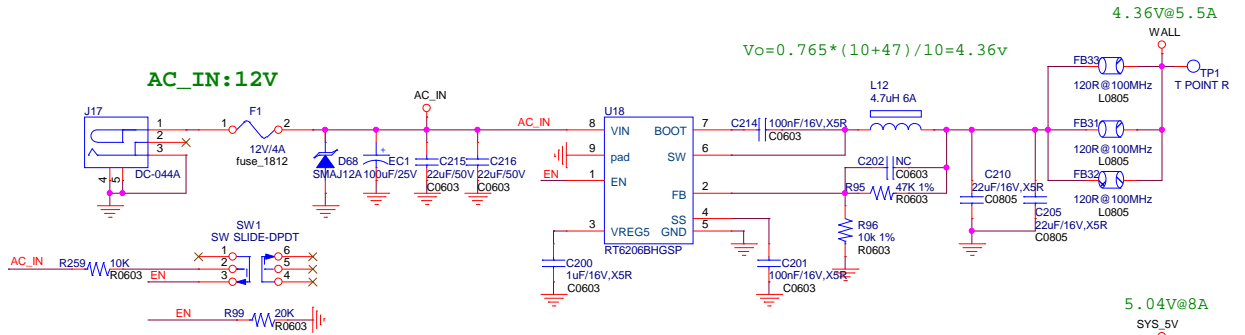
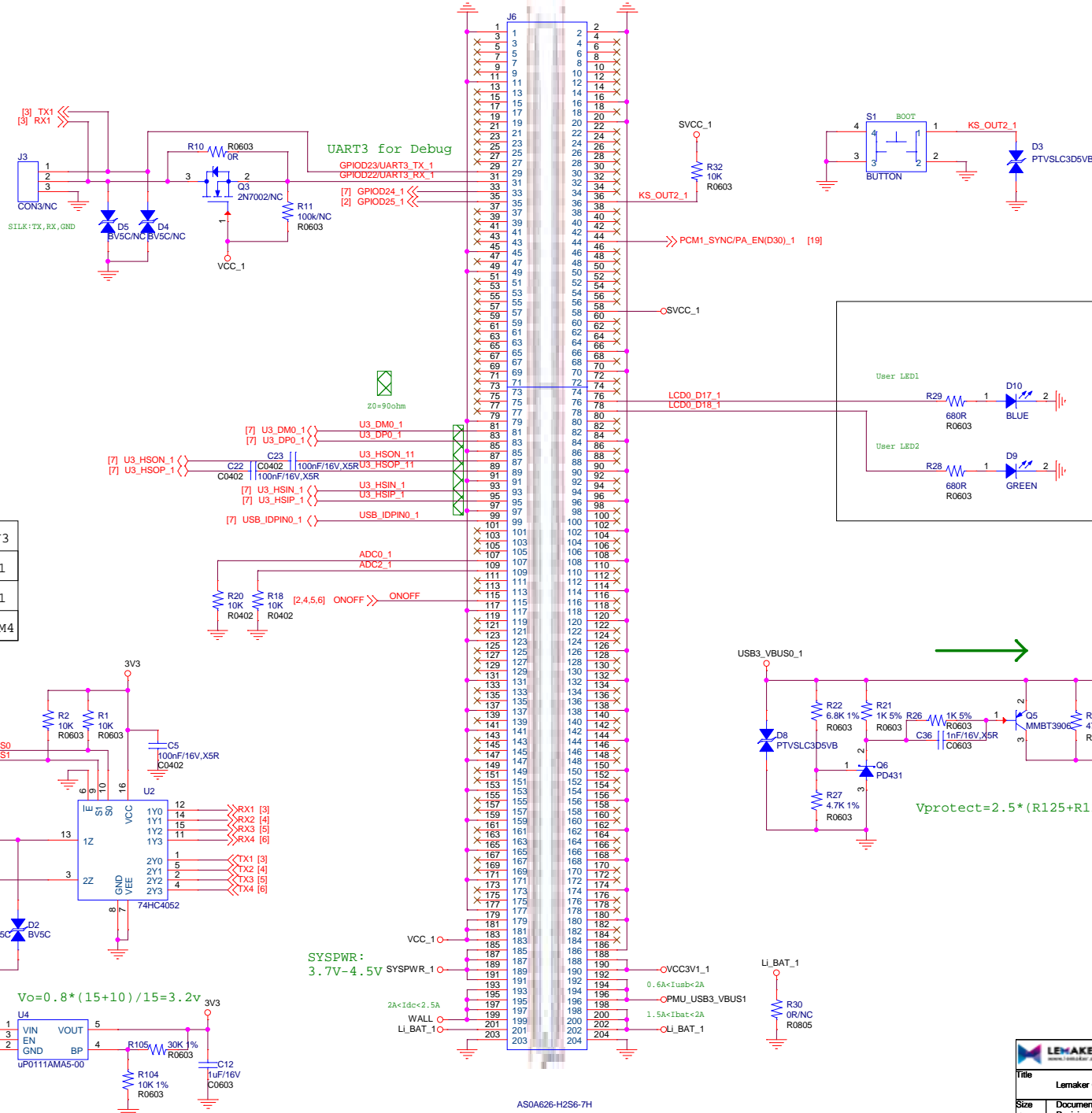


Revision	Date	Description
Rev 1.0	2015-04-08	第一版

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P05	USB	RN	: Resistor Networks
P06	Audio	D	: Diode
P07	WiFi	Q	: Transistor or FET
P08	PHY	X	: X-tal (Crystal)
P09	SODIMM 204P	M	: Module
		SW	: SWitch Tact/Push
		FB	: Ferrite Bead
		CON	: Connector
		LED	: LED Diode
		TP	: Test Point (SMD)
		CT	: Capacitor Tantal
		EC	: Electrolytic Capacitor



# Module 1

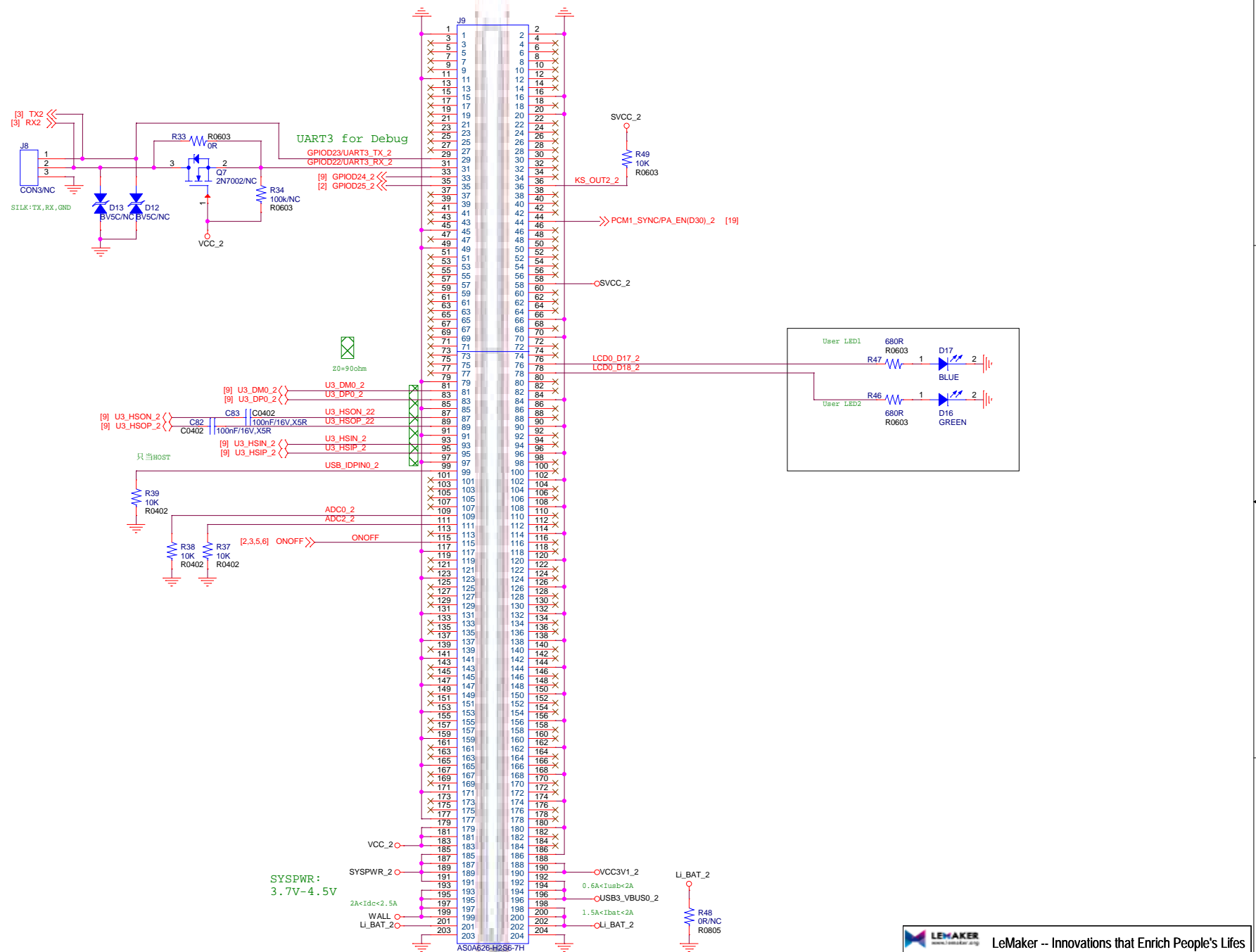


	Y0	Y1	Y2	Y3
S1	0	0	1	1
S0	0	1	0	1

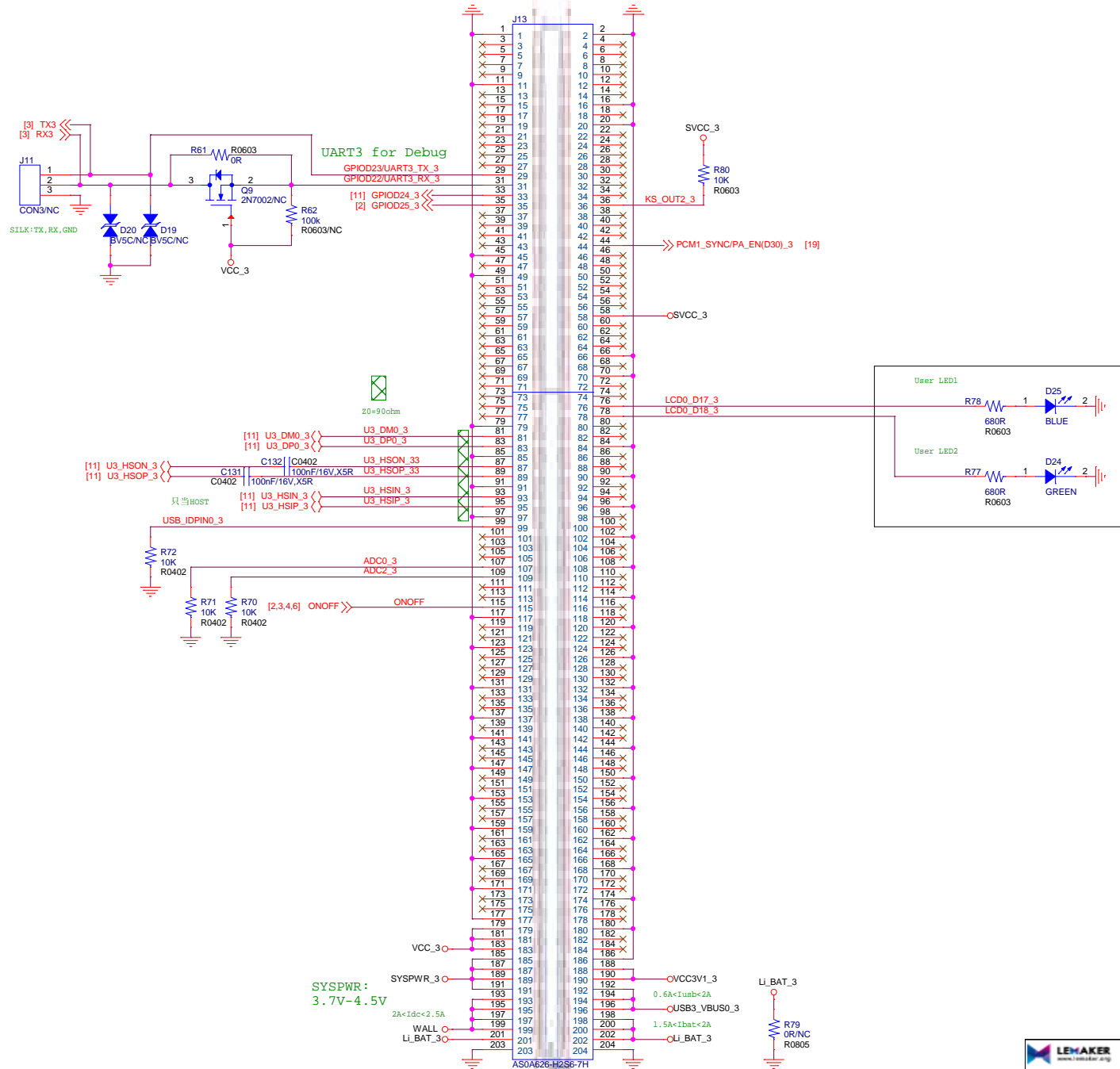
  

Uart	M1	M2	M3	M4

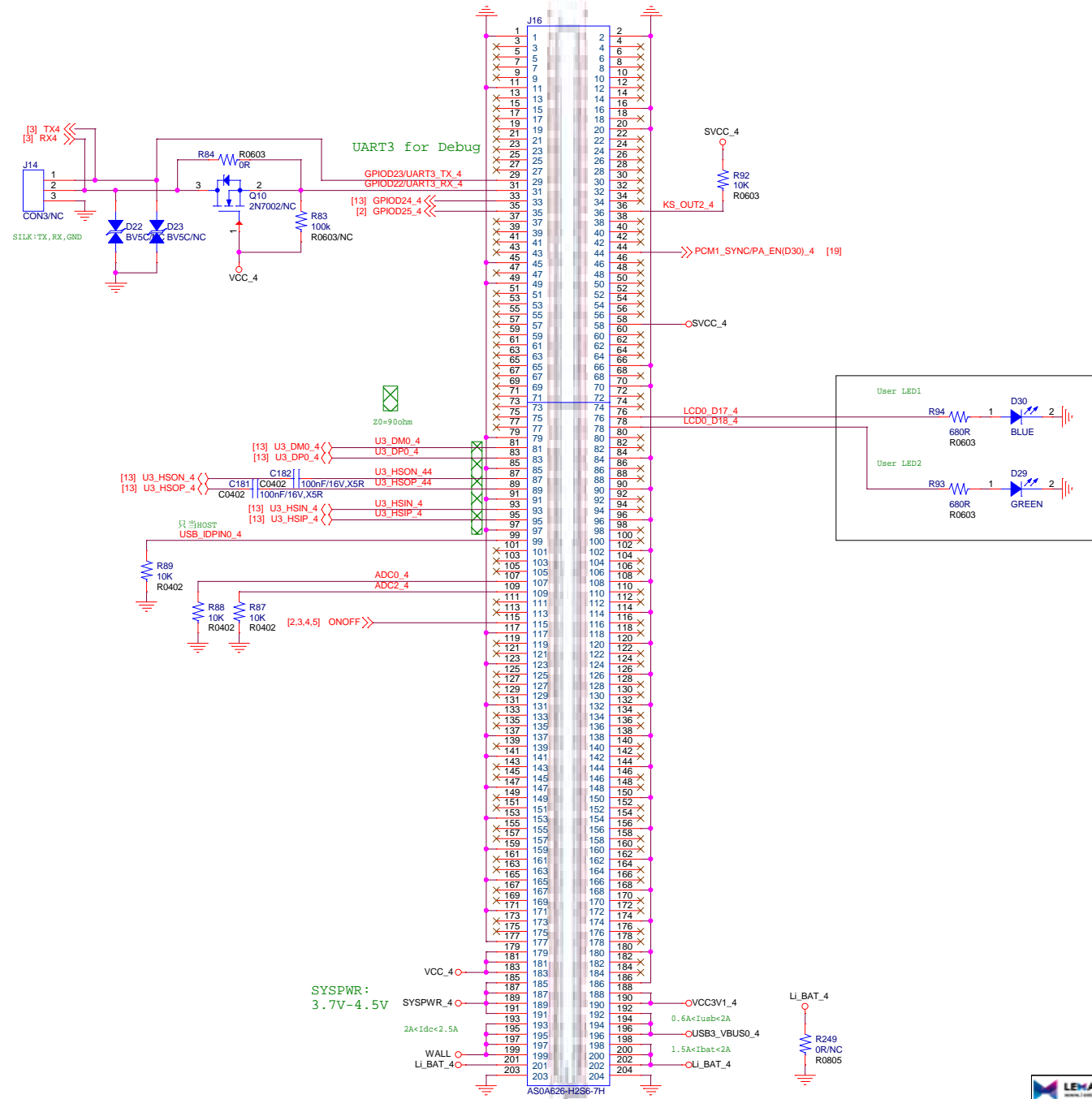
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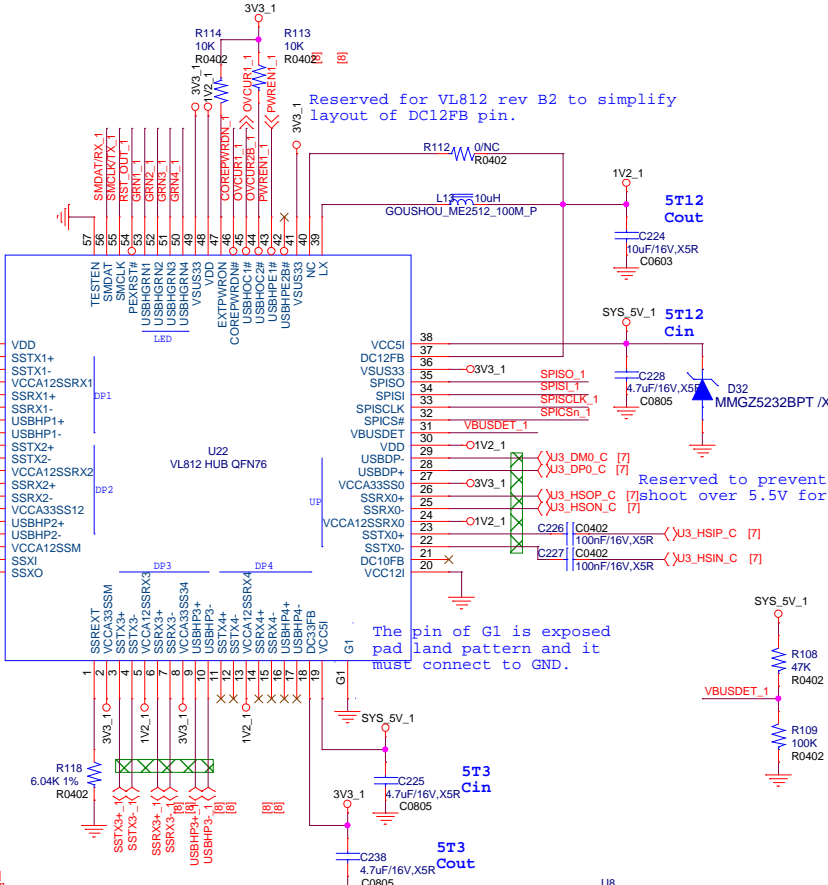
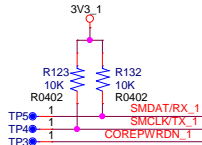
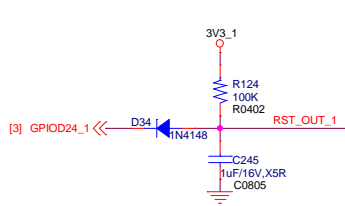


# Module 3



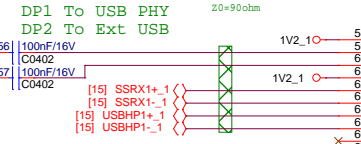
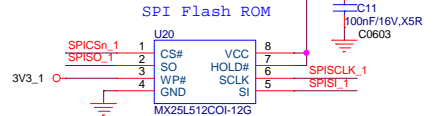
# Module 3



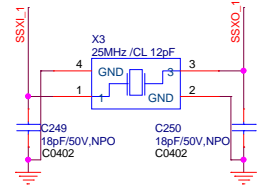
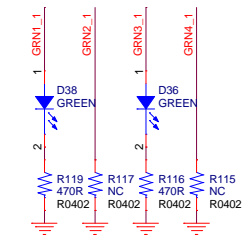


Reserved for VL812 rev B2 to simplify layout of DC12FB pin.

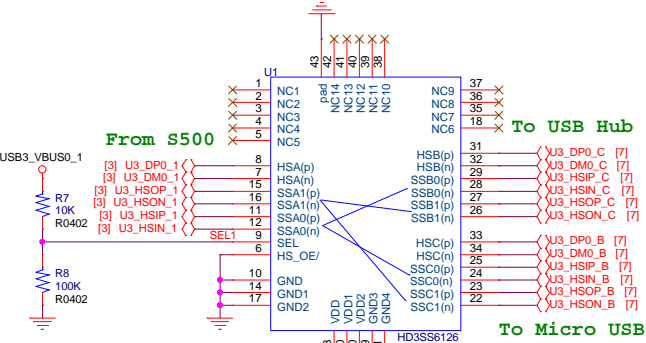
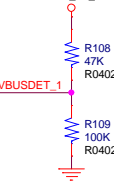
5T12 Cout  
5T12 Cin



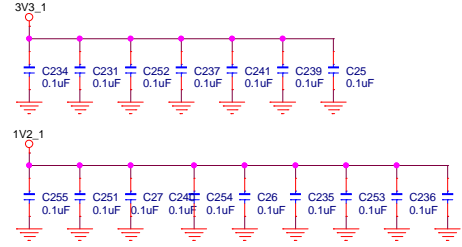
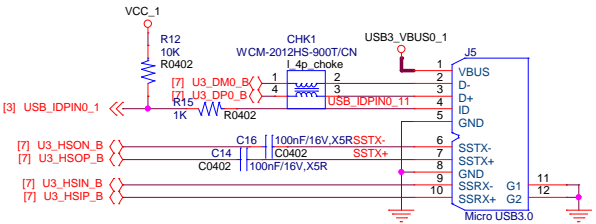
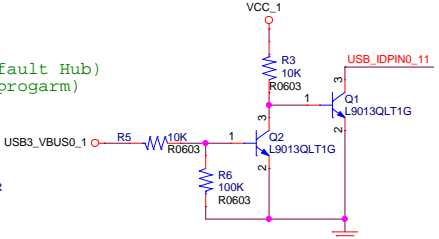
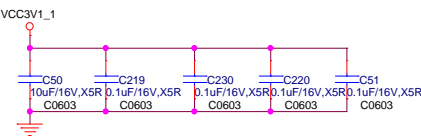
Reserved to prevent 5V input have over 5.5V for protect 5T12 DC2DC.

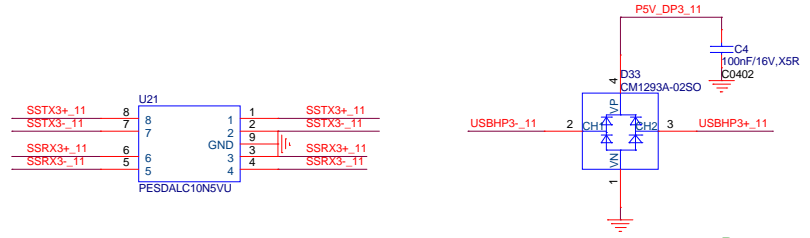


The pin of G1 is exposed pad land pattern and it must connect to GND.

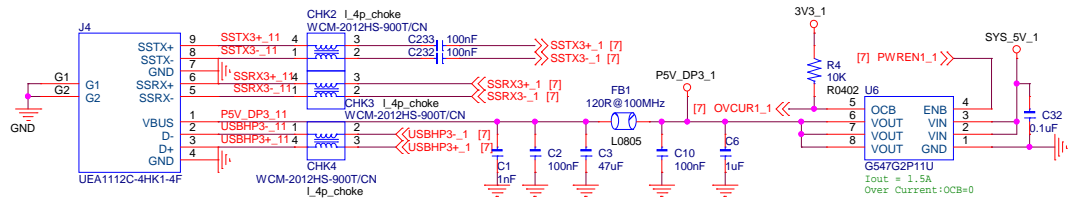


SEL:  
L:A<->B Host(default Hub)  
H:A<->C Device(program)

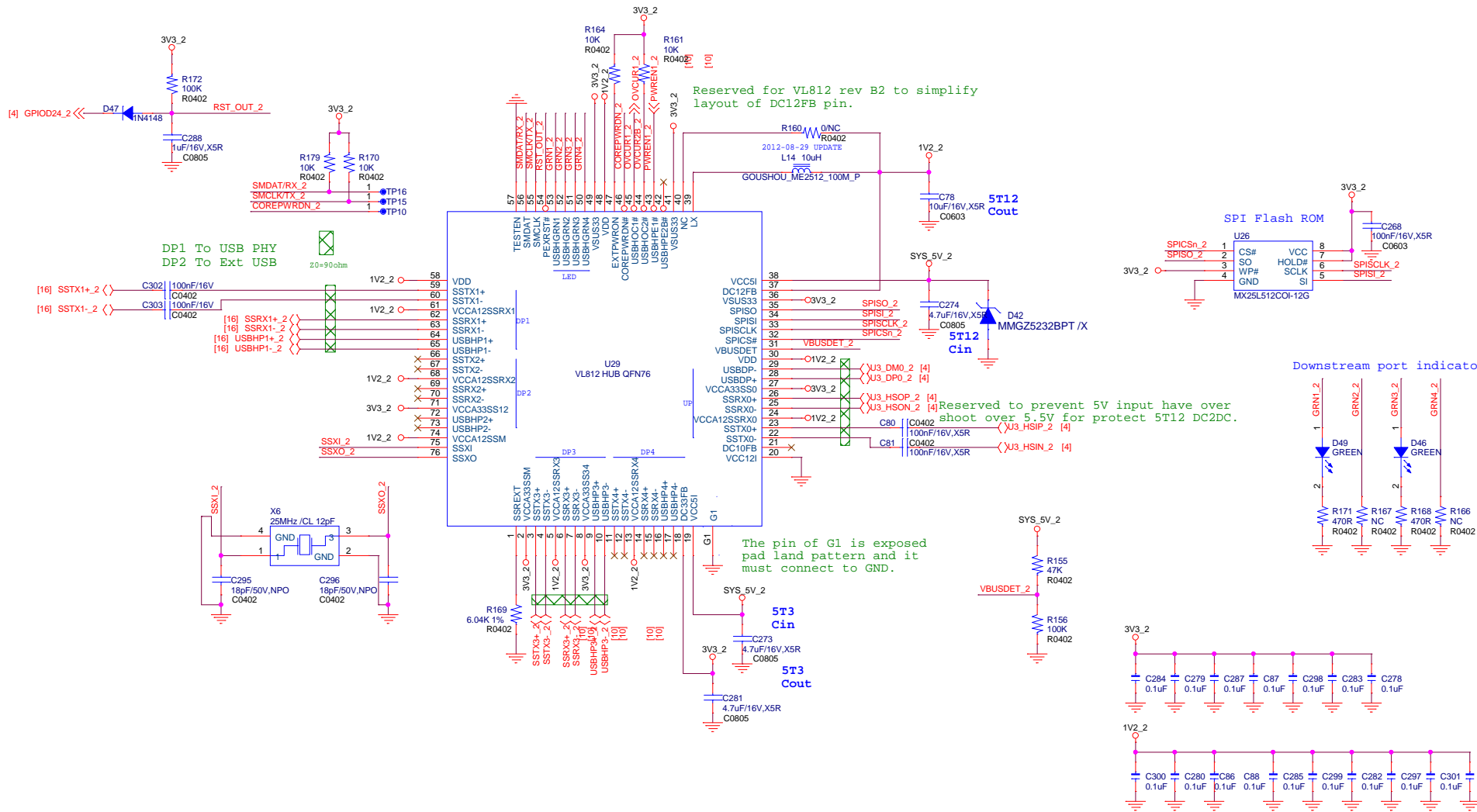




Power switch only  
low-active  
support.





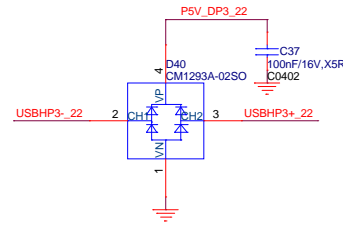
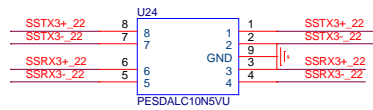


Reserved for VL812 rev B2 to simplify layout of DC12FB pin.

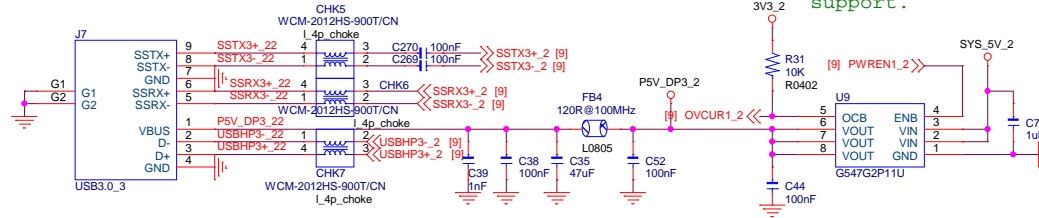
Reserved to prevent 5V input have over shoot over 5.5V for protect 5T12 DC2DC.

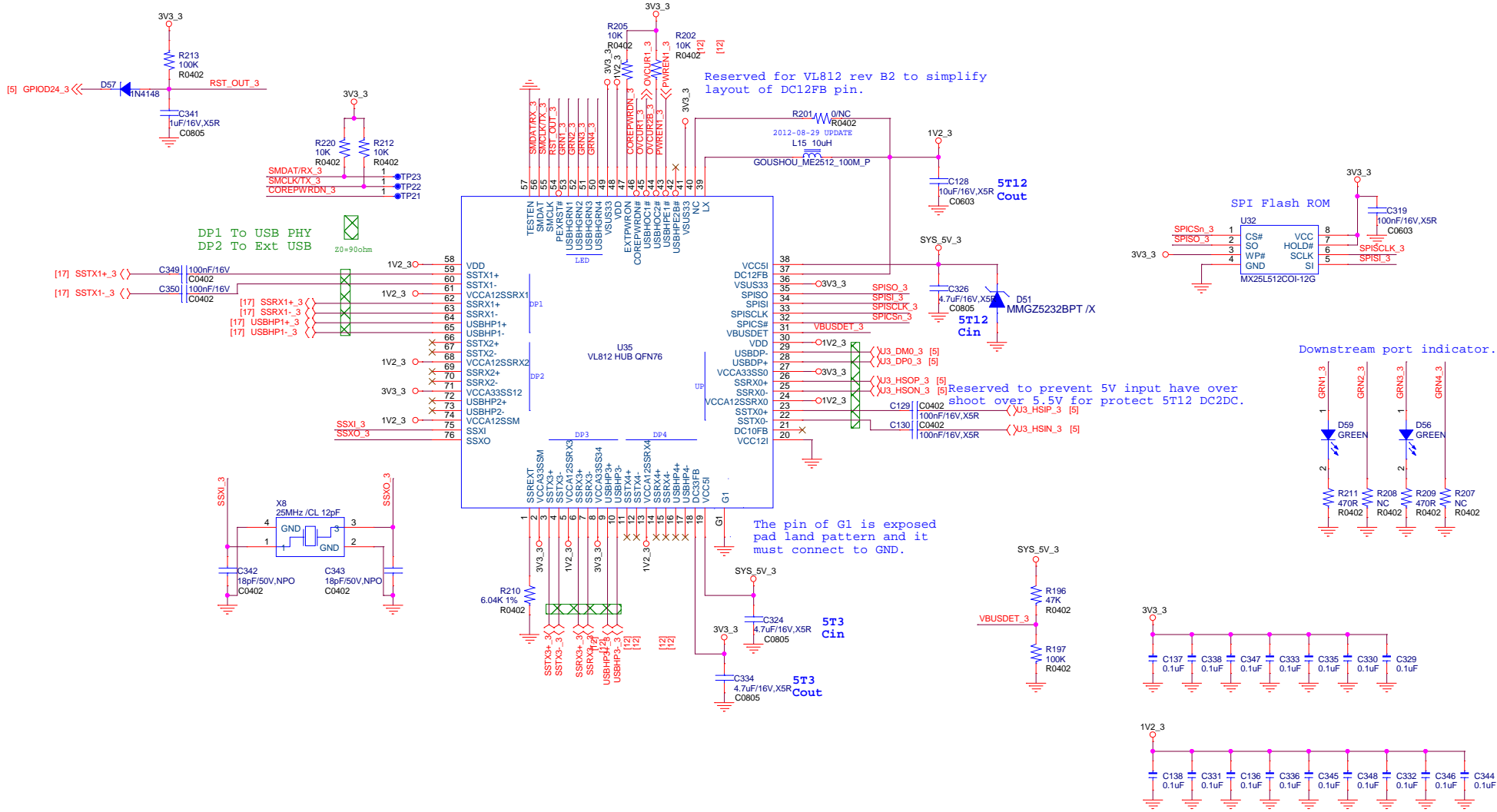
The pin of G1 is exposed pad land pattern and it must connect to GND.

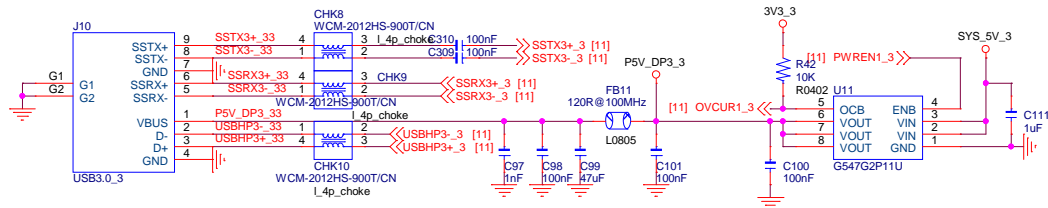
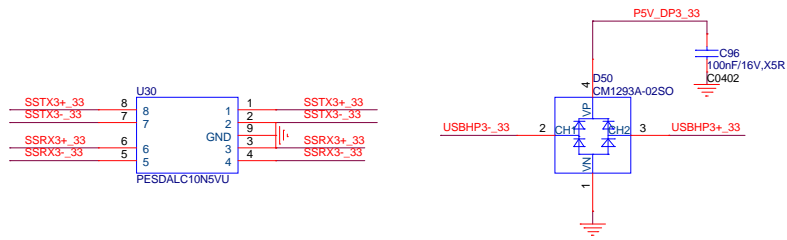
Downstream port indicator.



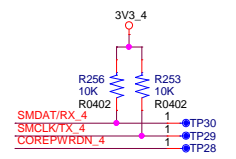
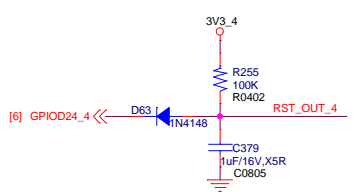
Power switch only  
low-active  
support.



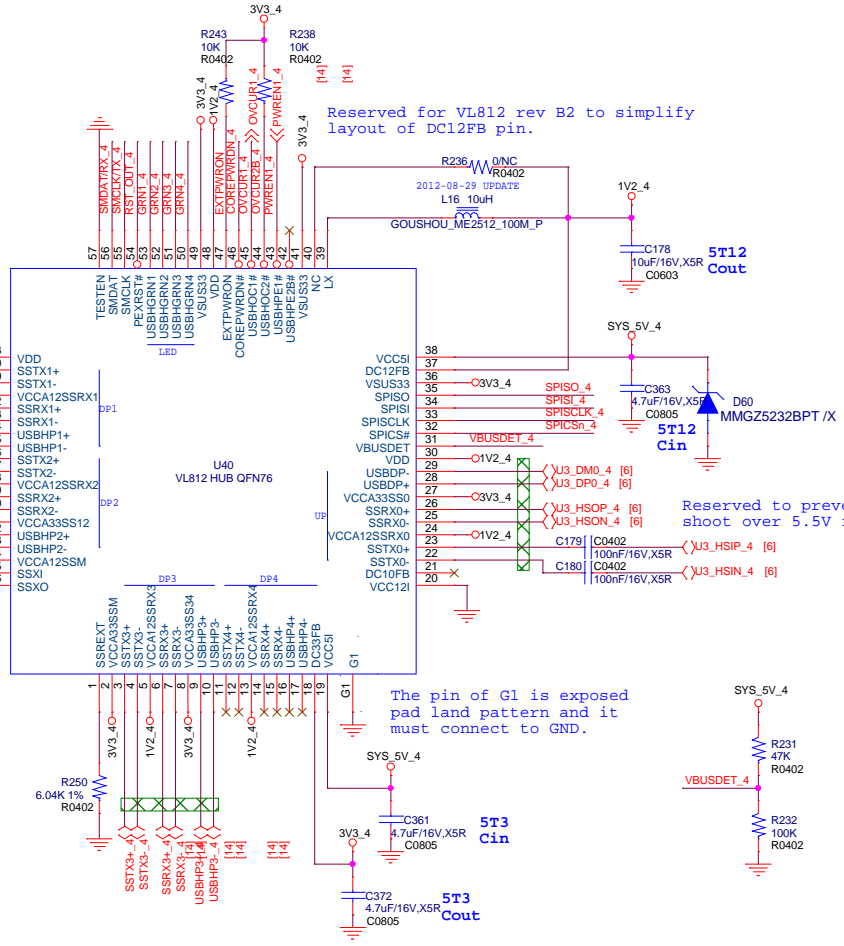
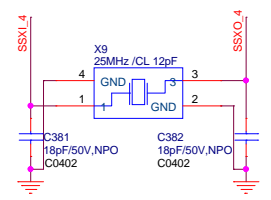
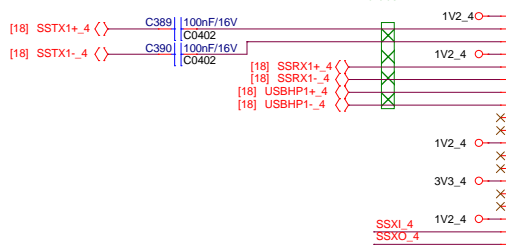




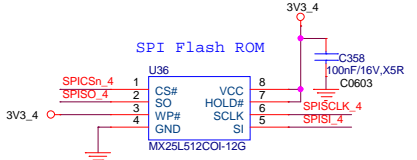
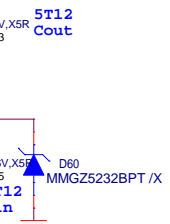
Power switch only  
low-active  
support.



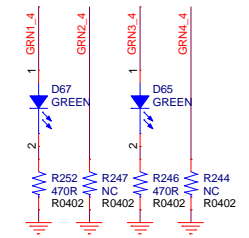
DP1 To USB PHY  
DP2 To Ext USB



Reserved for VL812 rev B2 to simplify layout of DC12FB pin.

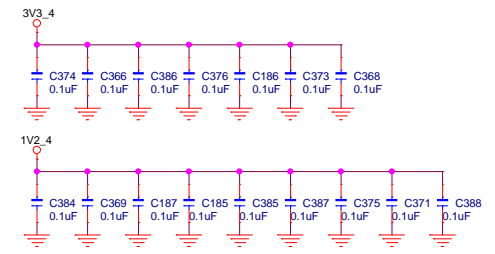
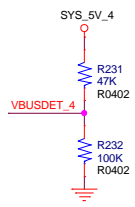


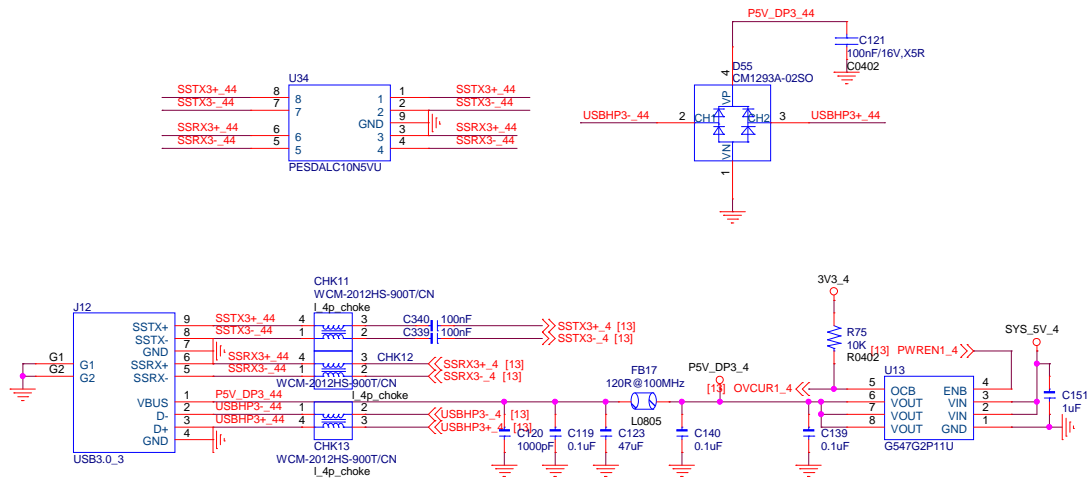
Downstream port indicator.



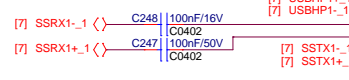
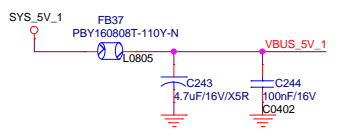
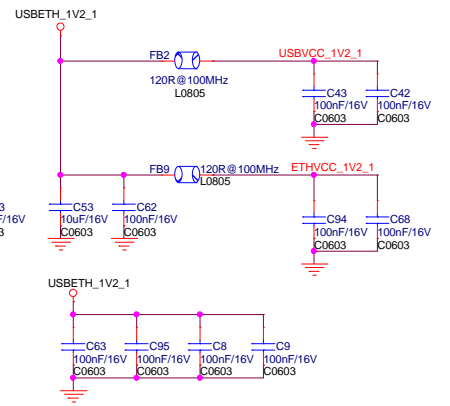
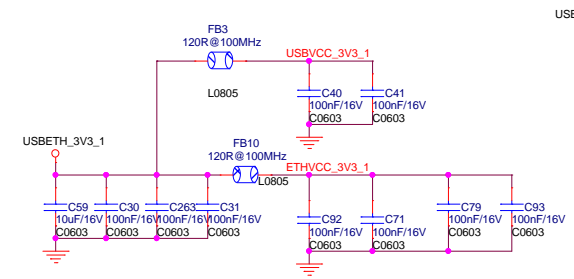
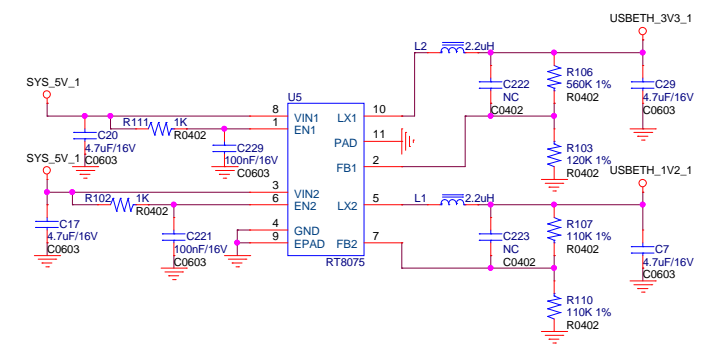
Reserved to prevent 5V input have over shoot over 5.5V for protect 5T12 DC2DC.

The pin of G1 is exposed pad land pattern and it must connect to GND.

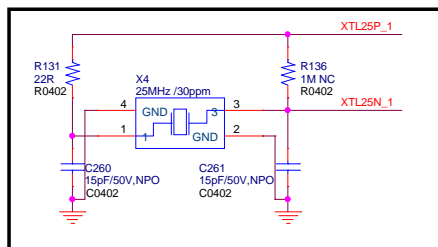




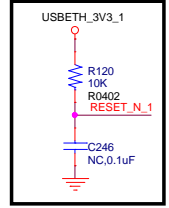
Power switch only  
low-active  
support.



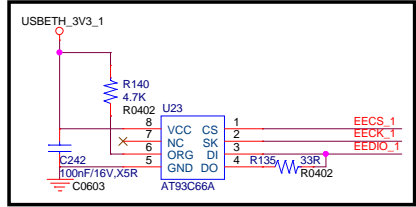
**25MHz +/- 30ppm Crystal**



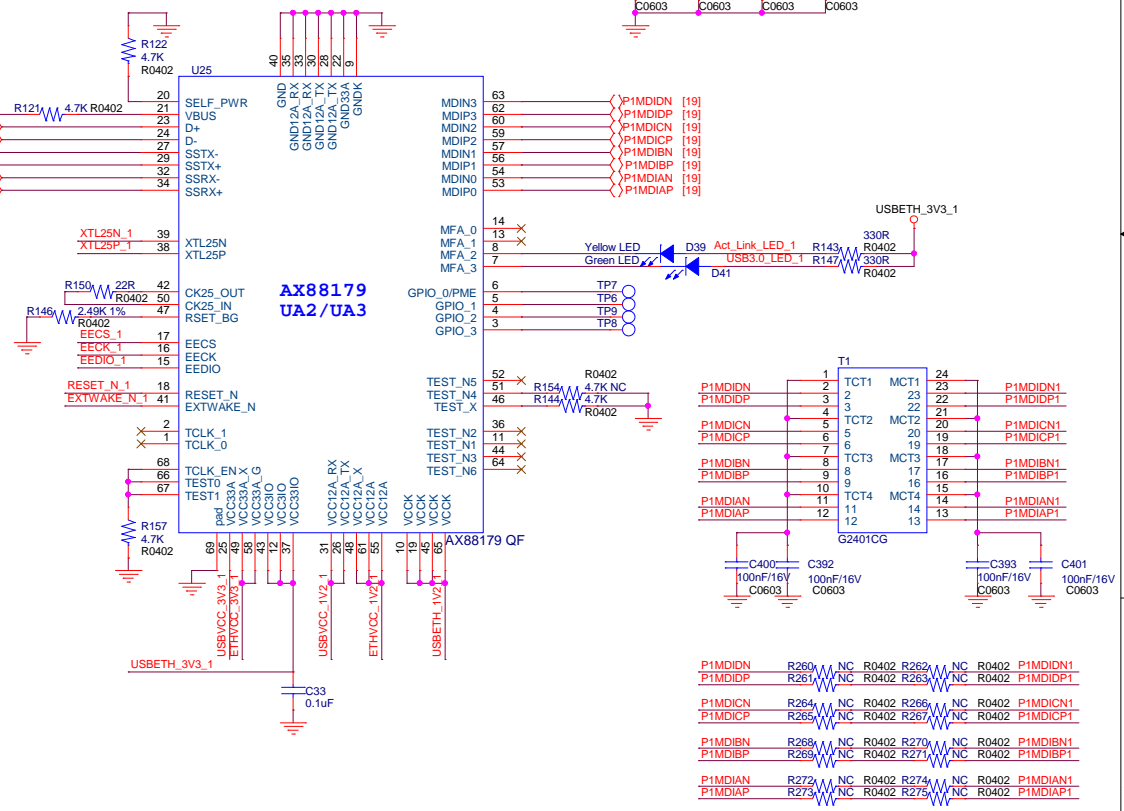
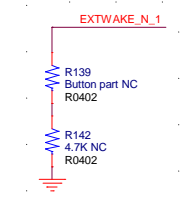
**Reset Circuit**

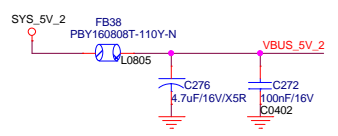
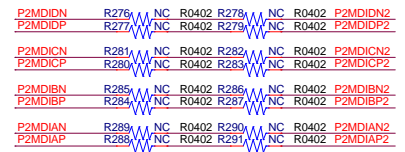
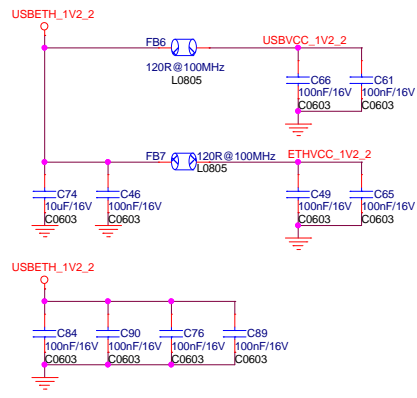
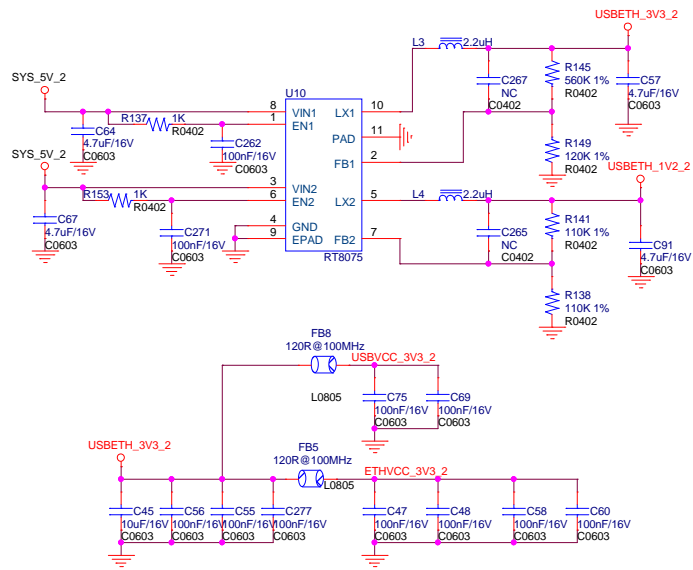


**93C56A or 93C66A EEPROM**

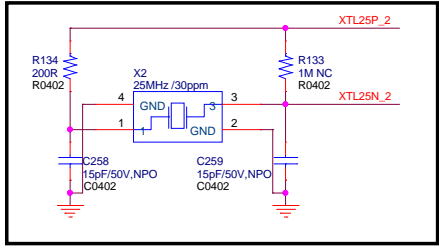


**Optional for USB-IF test**

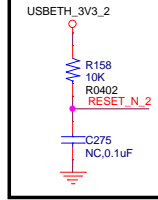




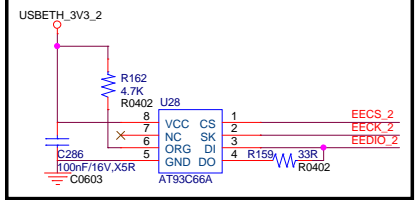
**25MHz +/- 30ppm Crystal**



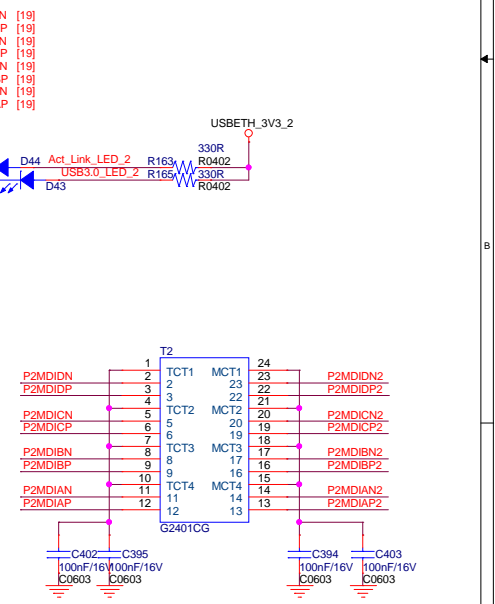
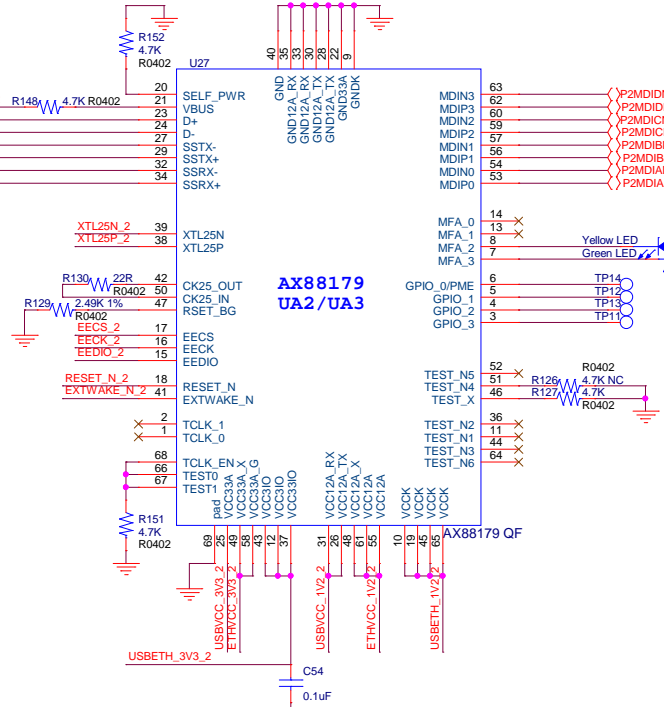
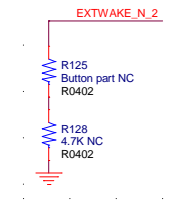
**Reset Circuit**



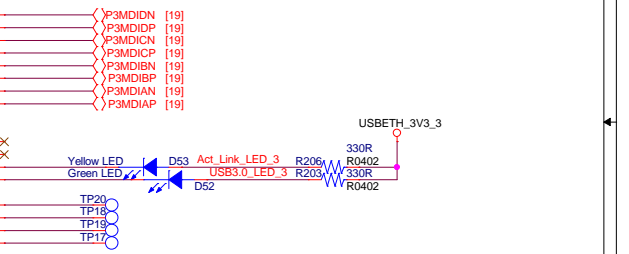
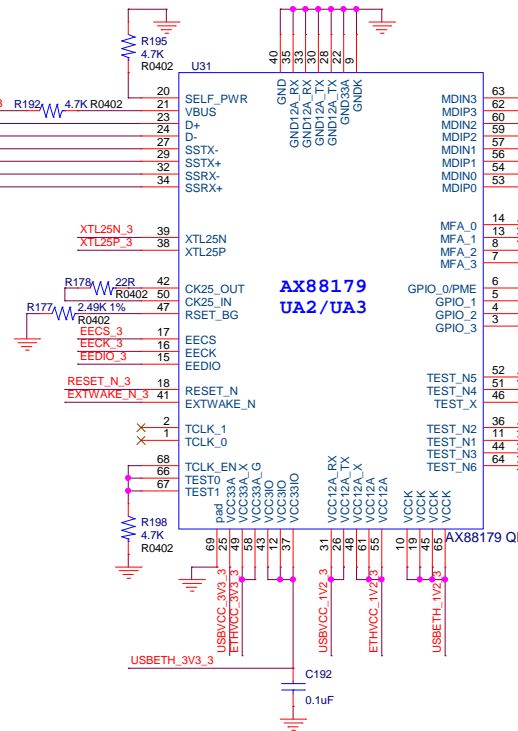
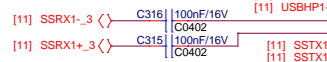
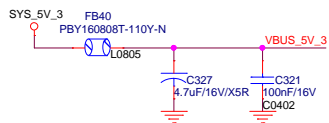
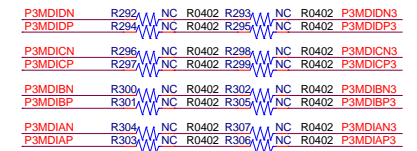
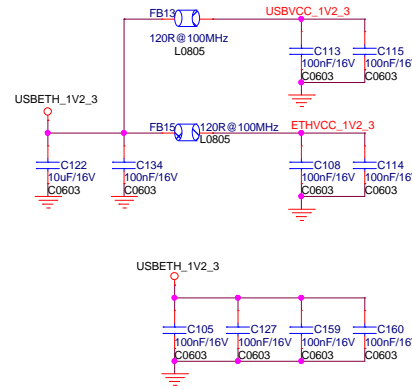
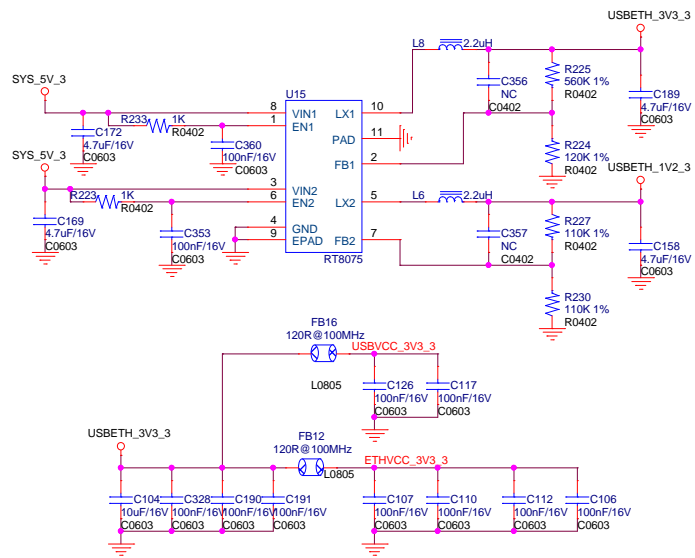
**93C56A or 93C66A EEPROM**



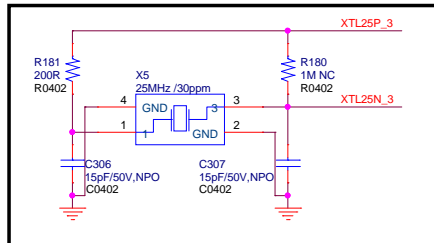
**Optional for USB-IF test**



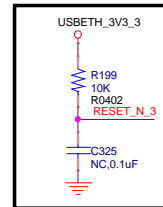




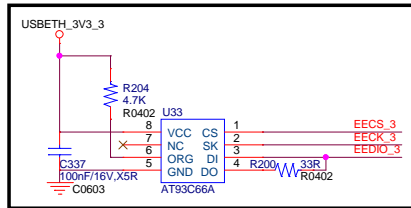
**25MHz +/- 30ppm Crystal**



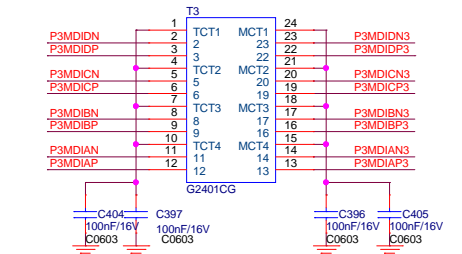
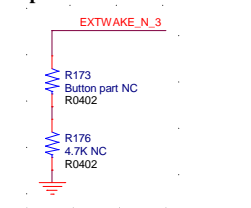
**Reset Circuit**

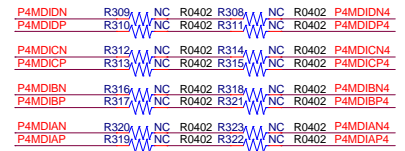
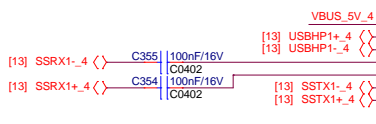
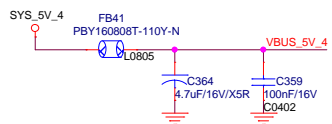
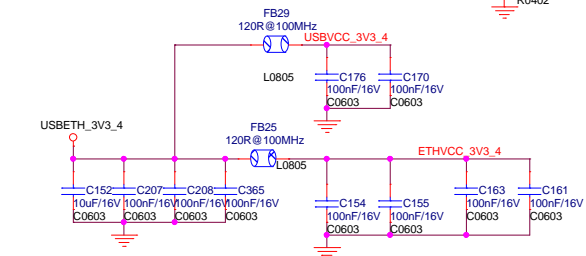
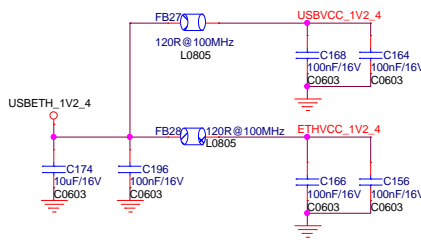
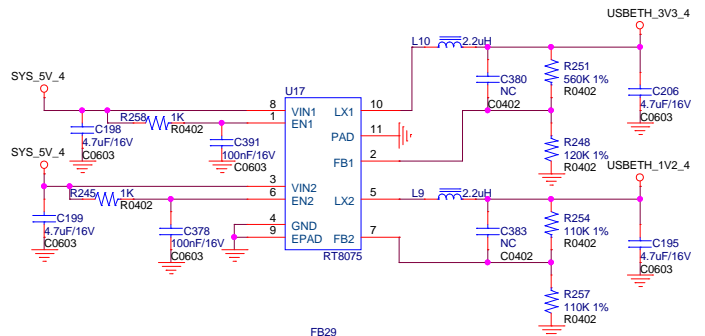


**93C56A or 93C66A EEPROM**

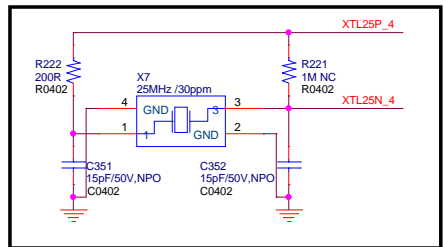


**Optional for USB-IF test**

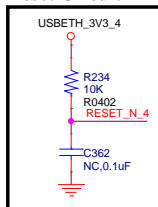




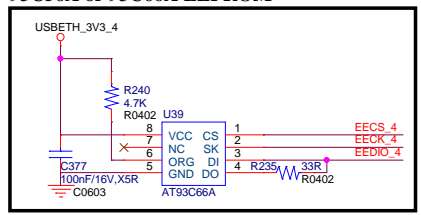
**25MHz +/- 30ppm Crystal**



**Reset Circuit**



**93C56A or 93C66A EEPROM**



**Optional for USB-IF test**

